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IN THE CLAIMS*The status of the claims as presently amended is as follows:*

1. (Currently Amended) A program operating apparatus comprising:

a first CPU ~~that has operation thereof controlled by a first control program;~~a second CPU ~~that has operation thereof controlled by a second control program;~~first storage means ~~that is rewritable, is removable from the program operating apparatus, and stores the first control program, the second control program, and a write control program, the first control program stored in said first storage means controlling operation of said first CPU in a normal mode;~~second storage means ~~for storing the second control program; and~~~~write control means operable when a mode of the apparatus is switched to a write control mode in which the second control program stored in said first storage means is written to said second storage means, for executing write control comprising transferring the write control program stored in said first storage means from said first storage means to said second CPU, and causing said second CPU to write the second control program stored in said first storage means to said second storage means,~~selecting means that selects a control mode of the apparatus,wherein said first storage means stores a first control program for controlling operation of said first CPU, a second control program for controlling operation of said second CPU, and a write control program for causing said second CPU to write the second control program to said second storage means,wherein said second storage means is capable of storing the second control program for causing said second CPU to operate, andwherein said first CPU operates in any of a normal mode in which said first CPU operates the first control program and a write control mode in which the write control program for causing said second CPU to write the second control program stored in said first storage means to said second storage means is transferred to said second CPU, according to the selection by said selecting device.2. (Currently Amended) A program operating apparatus according to claim 1, wherein said ~~write control means~~ first CPU is operable when ~~the mode of the apparatus is switched to the write control mode for transferring the write control program to said second CPU is selected by said selecting means, for transferring the write control program from said first storage means to for~~ executing a process of writing the second control program to said second storage means to a

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~~storage area accessible by said second CPU, and causing said second CPU to execute the~~
write control program transferred from said first storage means to thereby write the second
control program stored in said first storage means to said second storage means.

3. *(Canceled)*

4. *(Original)* A program operating apparatus according to claim 1, wherein said second storage
means comprises a rewritable non-volatile storage medium.

5. *(Original)* A program operating apparatus according to claim 4, wherein said second storage
means comprises a rewritable non-volatile storage medium provided in said second CPU.

6. *(Canceled)*

7. *(Original)* A program operating apparatus according to claim 1, which is applicable to an
image forming apparatus for forming images on sheets.

8. *(Original)* A program operating apparatus according to claim 7, wherein said image forming
apparatus comprises a main body, and at least one functional unit, and wherein said first CPU
controls basic operation of said main body of said image forming apparatus, and said second
CPU controls operations of said at least one functional unit.

9. *(Original)* A program operating apparatus according to claim 1, which is applicable to a
finisher for executing post-processing on sheets having images formed thereon.

10. *(Original)* A program operating apparatus according to claim 9, wherein said finisher
comprises a main body, and at least one functional unit, and wherein said first CPU controls
basic operation of said main body of said finisher, and said second CPU controls operations of
said at least one functional unit.

11. *(Currently Amended)* A program write control apparatus ~~installed on a program operating~~
apparatus comprising:

a first CPU; ~~that has operation thereof controlled by a first control program,~~

a second CPU; ~~that has operation thereof controlled by a second control program,~~

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~~first storage means that is rewritable, is removable from the program-operating write control apparatus, and stores the first control program, the second control program, and a write control program, and~~

~~second storage means for storing the second control program,~~

~~wherein the first control program stored in said first storage means stores a first control program for controlling operation of said first CPU, a second control program for controlling operation of said second CPU, and a write control program for causing said second CPU to write the second control program to said second storage means, in a normal mode, the program write control apparatus comprising,~~

~~wherein said second storage means is capable of storing the second control program for causing said second CPU to operate, and~~

~~wherein said first CPU operates the first control program stored in said removable first storage means in a normal mode, and said first CPU is write control means operable when a mode of the apparatus is switched to a write control mode in which the second control program stored in said first storage means is written to said second storage means, for executing write control comprising transferring the write control program stored in said first storage means from said first storage means to said second CPU, and causing said second CPU executes the write control program transferred from said first storage means to thereby write the second control program stored in said first storage means to said second storage means.~~

12. (Currently Amended) A program write control apparatus according to claim 11, wherein said ~~write control means~~ first CPU is operable when the mode of the apparatus is switched to the write control mode, for transferring the write control program ~~for executing a process of writing~~ causing said second CPU to write the second control program to said second storage means to a storage area accessible by said second CPU, ~~and causing said second CPU to execute the write control program to thereby write the second control program stored in said first storage means to said second storage means.~~

13. (Canceled)

14. (Original) A program write control apparatus according to claim 11, wherein said second storage means comprises a rewritable non-volatile storage medium.

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15. (*Original*) A program write control apparatus according to claim 14, wherein said second storage means comprises a rewritable non-volatile storage medium provided in said second CPU.

16. (*Currently Amended*) A program write control apparatus according to claim 11, wherein said ~~write control means~~ first CPU is operable when power is applied to ~~said program operating the apparatus to actuate same, for executing the write control~~ transferring the write control program to said second CPU if the mode of the apparatus has been switched to the write control mode by switching means for switching between ~~[[a]]~~ the normal control mode in which normal control of the apparatus is carried out and the write control mode.

17. (*Original*) A program write control apparatus according to claim 1, which is mounted in an image forming apparatus for forming images on sheets.

18. (*Original*) A program write control apparatus according to claim 17, wherein said image forming apparatus comprises a main body, and at least one functional unit, and wherein said first CPU controls basic operation of said main body of said image forming apparatus, and said second CPU controls operations of said at least one functional unit.

19. (*Original*) A program write control apparatus according to claim 1, which is mounted in a finisher for executing post-processing on sheets having images formed thereon.

20. (*Original*) A program write control apparatus according to claim 19, wherein said finisher comprises a main body, and at least one functional unit, and wherein said first CPU controls basic operation of said main body of said finisher, and said second CPU controls operations of said at least one functional unit.

21. (*Currently Amended*) A program write control method executed by a program operating apparatus comprising a first CPU that has operation thereof controlled by a first control program, a second CPU that has operation thereof controlled by a second control program, first storage means that is ~~rewritable~~, is removable from the program operating apparatus, and stores the first control program, the second control program, and a write control program, and second storage means ~~for~~ capable of storing the second control program, ~~wherein the first~~

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~~control program stored in said first storage means controls operation of said first CPU in a normal mode,~~ the method comprising the steps of:

controlling the operation of said first CPU by the first control program stored in said first storage means in a normal mode;

executing write control comprising transferring the write control program stored in said first storage means from said first storage means to said second CPU, and causing said second CPU to write the second control program stored in said first storage means to said second storage means when a mode of the apparatus is switched to a write control mode in which the second control program stored in said first storage means is written to said second storage means.

22. *(Currently Amended)* A program write control method according to claim ~~[[22]]~~ 21, wherein said step of executing the write control comprises causing said first CPU to transferring the write control program for executing a process of writing the second control program to said second storage means to a storage area accessible by said second CPU, and causing said second CPU to execute the write control program to thereby write the second control program stored in said first storage means to said second storage means, when the mode of the apparatus is switched to the write control mode.

23. *(Original)* A program write control method according to claim 21, which is executed by an image forming apparatus for forming images on sheets.

24. *(Original)* A program write control method according to claim 21, which is executed by a finisher for executing post-processing on sheets having images formed thereon.

25. *(Original)* A program write control method according to claim 23, wherein said image forming apparatus comprises a main body, and at least one functional unit, and wherein said first CPU controls basic operation of said main body of said image forming apparatus, and said second CPU controls operations of said at least one functional unit.

26. *(Original)* A program write control method according to claim 24, wherein said finisher comprises a main body, and at least one functional unit, and wherein said first CPU controls basic operation of said main body of said finisher, and said second CPU controls operations of said at least one functional unit.

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27. (*Currently Amended*) A program operating apparatus comprising:

at least one functional unit;

a first CPU ~~that controls basic operation of the apparatus according to a first control program;~~

a second CPU ~~that controls operation of said functional unit according to a second control program;~~

first storage means that is ~~rewritable~~, is removable from the program operating apparatus, ~~and stores the first control program and the second control program, the first control program stored in said first storage means controlling operation of said first CPU in a normal mode; and~~

~~second storage means for storing the second control program; and,~~

wherein said first storage means stores a first control program for controlling operation of said first CPU to thereby control basic operation of the entire apparatus, a second control program for controlling operation of said second CPU to thereby control operation of said functional unit, and a write control program for causing said second CPU to write the second control program to said second storage means,

wherein said second storage means is capable of storing the second control program for causing said second CPU to operate, and

wherein said first CPU operates the first control program stored in said first storage means in a normal mode, and said first CPU is write-control-means operable when a mode of the apparatus is switched to a write control mode in which the second control program stored in said first storage means is written to said second storage means, for controlling writing, transferring the write control program stored in said first storage means to said second CPU, and said second CPU executes the write control program transferred from said first storage means to thereby write the second control program stored in said first storage means to said second storage means.

28. (*Original*) A program operating apparatus according to claim 27, which is applicable to an image forming apparatus for forming images on sheets.

29. (*Original*) A program operating apparatus according to claim 27, which is applicable to a finisher for executing post-processing on sheets having images formed thereon.

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30. (*Currently Amended*) An image forming apparatus comprising:

at least one functional unit;

a first CPU ~~that controls basic operation of the apparatus according to a first control program;~~

a second CPU ~~that controls operation of said functional unit according to a second control program;~~

first storage means that is ~~rewritable, is removable from the image forming apparatus, and stores the first control program, the second control program, and a write control program, the first control program stored in said first storage means controlling operation of said first CPU in a normal mode; and~~

second storage means ~~for storing the second control program; and,~~

wherein said first storage means stores a first control program for controlling operation of said first CPU to thereby control basic operation of the entire apparatus, a second control program for controlling operation of said second CPU to thereby control operation of said functional unit, and a write control program for causing said second CPU to write the second control program to said second storage means,

wherein said second storage means is capable of storing the second control program for causing said second CPU to operate, and

wherein said first CPU operates the first control program stored in said first storage means in a normal mode, and said first CPU is write control means operable when a mode of the apparatus is switched to a write control mode in which the second control program stored in said first storage means is written to said second storage means, for executing write control comprising transferring the write control program stored in said first storage means from said first storage means to said second CPU, and causing said second CPU executes the write control program transferred from said first storage means to thereby write the second control program stored in said first storage means to said second storage means.

31. (*Currently Amended*) A finisher comprising:

at least one functional unit;

a first CPU ~~that controls basic operation of the finisher according to a first control program;~~

a second CPU ~~that controls operation of said functional unit according to a second control program;~~

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~~first storage means that is rewritable, is removable from the finisher, and stores the first control program, the second control program, and a write control program, the first control program stored in said first storage means controlling operation of said first CPU in a normal mode; and~~

~~second storage means for storing the second control program; and,~~

wherein said first storage means stores a first control program for controlling operation of said first CPU to thereby control basic operation of the entire finisher, a second control program for controlling operation of said second CPU to thereby control operation of said functional unit, and a write control program for causing said second CPU to write the second control program to said second storage means,

wherein said second storage means is capable of storing the second control program for causing said second CPU to operate, and

wherein said first CPU operates the first control program stored in said first storage means in a normal mode, and said first CPU is write control means operable when a mode of the finisher is switched to a write control mode in which the second control program stored in said first storage means is written to said second storage means, for executing write control comprising transferring the write control program stored in said first storage means from said first storage means to said second CPU, and said second CPU executes the write control program transferred from said first storage means causing said second CPU to thereby write the second control program stored in said first storage means to said second storage means.